

REMARKS

Applicants acknowledges with appreciation that claims 3-15 are allowed.

Applicants have amended Claims 2, 4, 7 and 16 to correct typographical errors and added Claims 17-20. Therefore, upon entry of this amendment, Claims 1-20 are pending.

Applicants respectfully request reconsideration and reexamination of the application.

Claim 1 was provisionally rejected under obviousness type double patenting with respect to claims 16 and 18 of co-pending Application No. 10/793,860. A first Terminal Disclaimer is included herewith disclaiming the terminal part of the statutory term of a patent granted based on the present application that may extend beyond the expiration date of any patent granted on a pending reference application number 10/793,860. Therefore, Applicants respectfully request the provisional double patenting rejection be withdrawn.

Claims 1 and 2 were rejected under obviousness type double patenting with respect to U.S. Patent No. 6,995,394. A second Terminal Disclaimer is included herewith disclaiming the terminal part of the statutory term of a patent granted based on the present application that may extend beyond the expiration date of U.S. Patent No. 6,995,394. Therefore, Applicants respectfully request the double patenting rejection be withdrawn.

The specification was objected to under 37 CFR 1.75(d)(1) and MPEP § 608.01(o) for failing to provide proper antecedent basis for the claimed subject matter in reference to an element of Claim 16. Claim 16 is amended to remove a typographical error and to conform to the specification and drawings as filed. Applicants respectfully assert that the amended step of "patterning the amorphous silicon layer, the contact layer,

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and the metal conductive layer to form a data wire, a direction control electrode, and a channel portion of a thin film transistor" is described in the specification from page 14 line 27 to page 15 line 5, also corresponding to paragraphs [0074]-[0075]. The abbreviation DCE for "direction control electrode" is introduced on page 7 line 16, also corresponding to paragraph [0034]. Further description of forming the thin film transistor (TFT) channel in the amorphous silicon layer is described in the specification on page 11 lines 11-18, also corresponding to paragraph [0056]. The specification paragraph on page 5 lines 25-31, also corresponding to paragraph [0020], is amended to conform to the amended Claim 16. Claims 2, 4 and 7 were amended only to correct typographical errors. No new matter is introduced by these amendments. Therefore, Applicants respectfully request the specification objection be withdrawn.

Claim 16 was rejected under 35 USC § 112 for being indefinite corresponding to the above described specification objection. Claim 16 was amended as discussed above. Therefore, Applicants respectfully request the rejection under 35 USC § 112 be withdrawn.

New claims 17-20 are added to recite elements of allowed Claim 4 and 7. Specifically, Claim 17 depends from Claim 16 and recites the elements of allowed Claim 4, while Claim 18 depends from Claim 16 and recites the elements of allowed Claim 7. Claim 19 depends from Claim 1 and recites the elements of allowed Claim 4. Finally Claim 20 depends from Claim 9 and recites the elements of allowed Claim 4.

Accordingly, Applicant respectfully submits that Claims 1-20 are in proper form for allowance. Reconsideration and withdrawal of the objections and rejections are respectfully requested and a timely Notice of Allowance is solicited.

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If the Examiner has any questions or concerns, a telephone call to the undersigned at (949) 752-7040 is welcomed and encouraged.

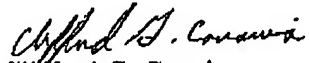
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I hereby certify that this paper is being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

  
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**June 21, 2006**  
Date of Signature

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